

Electronic Acknowledgement Receipt

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| International Application Number: | |
| Confirmation Number: | 7529 |
| Title of Invention: | Method and apparatus for automatic generation of multiple integrated circuit simulation configuration |
| First Named Inventor/Applicant Name: | Rajarshi Bhattacharya |
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| Document Number | Document Description | File Name | File Size(Bytes) /Message Digest | Multi Part /zip | Pages (if appl.) |
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| 1 | Reply Brief Filed | 1150-1145_RB.pdf | 321812 b6ec7835c07d44635054112246a2839b b7aef934 | no | 6 |

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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